

~~PATENT NUMBER~~

U.S. UTILITY Patent Application

JC 2
JC 858

O.I.P.F. <u>71</u> SCANNED <u>40</u> Q.A. <u>41</u>	PATENT DATE
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APPLICATION NO. 09/729080	CONT/PRIOR	CLASS 708 36	SUBCLASS 1	ART UNIT 2121	EXAMINER <i>Thompson</i>
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S Kazutaka Saitoh

APPLICANTS

Smart card and circuitry layout thereof

TITLE

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		_____ (Date)	
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